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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	09/450,381		
Filing Date	November 29, 1999		
First Named Inventor	Rajesh R. Shah		
Art Unit	2126		
Examiner Name	Anya, Charles E.		
Attomey Docket Number	42200D7710		

Complete if Known

NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²				
CA	1	Next Generation I/O Architecture Specification: Channel Interface Specification, Final Draft: March 26, 1999.					
CA	2	Next Generation I/O Link Architecture Specification: Fabric Service Interface Specification, Final Draft: March 26, 1999.					
CE	3	Next Generation I/O Link Architecture Specification: HCA Specification, Final Draft: March 26, 1999.					
CA	4	Next Generation I/O Link Architecture Specification: Link Specification, Final Draft: March 26, 1999.					
CA	5	Next Generation I/O: Principles of Operation, Final Draft: March 26, 1999.					
CA	6	Next Generation I/O Architecture: Switch Specification, Final Draft: March 26, 1999.					
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Examiner Signature	CHARLES	ANYA	Date Considered	12/13/04	

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if nown conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/S8/08B (08-03) as modified by Blakely, Solokoff, Taytor & Zafman (wtr) 08/11/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450